

Erledigt

Lösung für "X86Platform ... Failed to send stepper" mit einer "boardfremden" CPU

Beitrag von „amadeusex“ vom 16. November 2020, 00:17

[bluebyte](#)

Ich hab' jetzt mal ein bisschen rumgedoktert aber so wie es aussieht klappt es immer noch nicht mit den niedrigeren Frequenzen wenn ich MacPro 7,1 als Symbios waehle. Das System ist zwar ein wenig schneller als mit dem MacPro 6,1 Symbios aber die Sensoren zeigen immer CPU-Frequenzen oberhalb von 2 GHz an.

Hier mal die Ausgabe vom AppleIntelInfo.kext:

Symbios MacPro 7,1

Code

1. Theos-Mac-Pro:~ theomitrentsis\$ sudo cat /tmp/AppleIntelInfo.dat
2. AppleIntelInfo.kext v2.9 Copyright © 2012-2017 Pike R. Alpha. All rights reserved.
3. logIGPU.....: 0
4. logCStates.....: 1
5. logIPGStyle.....: 1
- 6.
7. CPU Ratio Info:
8. -----
9. Base Clock Frequency (BLCK)..... : 100 MHz
10. Maximum Efficiency Ratio/Frequency.....: 12 (1200 MHz)
11. Maximum non-Turbo Ratio/Frequency.....: 34 (3400 MHz)
12. Maximum Turbo Ratio/Frequency.....: 40 (4000 MHz)
13. P-State ratio * 100 = Frequency in MHz
14. -----
15. CPU P-States [(12) 34 37]
16. CPU C6-Cores [9]
17. CPU P-States [12 34 36 37 (39)]
18. CPU C6-Cores [4 5 8 9]
19. CPU C6-Cores [4 5 7 8 9 10]
20. CPU P-States [(12) 34 36 37 38 39]
21. CPU C6-Cores [4 5 6 7 8 9 10 11 13]
22. CPU P-States [(12) 25 34 36 37 38 39]
23. CPU C6-Cores [3 4 5 6 7 8 9 10 11 13 15]

24. CPU P-States [12 25 26 34 36 37 38 (39)]
25. CPU C6-Cores [1 3 4 5 6 7 8 9 10 11 12 13 15]
26. CPU P-States [(12) 25 26 27 34 36 37 38 39]
27. CPU C6-Cores [1 3 4 5 6 7 8 9 10 11 12 13 14 15]
28. CPU P-States [(12) 25 26 27 32 34 36 37 38 39]
29. CPU C6-Cores [0 1 3 4 5 6 7 8 9 10 11 12 13 14 15]
30. CPU P-States [(12) 25 26 27 28 32 34 36 37 38 39]
31. CPU P-States [(12) 25 26 27 28 29 32 34 36 37 38 39]
32. CPU P-States [12 23 25 26 27 28 29 32 34 36 37 38 (39)]
33. CPU C6-Cores [0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15]
34. CPU P-States [(12) 23 25 26 27 28 29 31 32 34 36 37 38 39]
35. CPU P-States [12 23 24 25 26 27 28 29 31 32 34 36 37 38 (39)]
36. CPU P-States [(12) 22 23 24 25 26 27 28 29 31 32 34 36 37 38 39]
37. CPU P-States [(12) 22 23 24 25 26 27 28 29 30 31 32 34 36 37 38 39]
38. CPU P-States [12 22 23 24 25 26 27 28 29 30 31 32 34 36 37 38 39 (40)]
39. CPU P-States [12 22 23 24 25 26 27 28 29 30 31 32 34 35 36 37 (38) 39 40]
40. CPU P-States [12 22 23 24 25 26 27 28 29 30 31 32 33 34 35 36 37 38 (39) 40]
41. Theos-Mac-Pro:~ theomitrentsis\$

Alles anzeigen

Zum Vergleich hier mit dem Macpro 6,1 symbios:

Code

1. AppleIntelInfo.kext v2.9 Copyright © 2012-2017 Pike R. Alpha. All rights reserved.
2. logIGPU.....: 0
3. logCStates.....: 1
4. logIPGStyle.....: 1
- 5.
6. CPU Ratio Info:
7. -----
8. Base Clock Frequency (BLCK)..... : 100 MHz
9. Maximum Efficiency Ratio/Frequency.....: 12 (1200 MHz)
10. Maximum non-Turbo Ratio/Frequency.....: 34 (3400 MHz)
11. Maximum Turbo Ratio/Frequency.....: 40 (4000 MHz)
12. P-State ratio * 100 = Frequency in MHz
13. -----
14. CPU P-States [(23) 33 36]
15. CPU C3-Cores [0 1]
16. CPU C6-Cores [2 6 9 11 14 15]
17. CPU P-States [(23) 33 34 36]

18. CPU C6-Cores [2 6 7 9 11 14 15]
19. CPU P-States [(12) 23 33 34 36]
20. CPU C6-Cores [2 6 7 9 11 14 15]
21. CPU P-States [(12) 23 26 33 34 36]
22. CPU C6-Cores [2 3 6 7 9 11 13 14 15]
23. CPU C6-Cores [1 2 3 6 7 9 11 13 14 15]
24. CPU C6-Cores [1 2 3 4 6 7 9 11 13 14 15]
25. CPU P-States [(12) 23 26 32 33 34 36]
26. CPU P-States [12 23 26 (29) 31 32 33 34 36]
27. CPU C6-Cores [1 2 3 4 6 7 9 11 12 13 14 15]
28. CPU P-States [12 23 26 29 31 32 33 34 36 (39)]
29. CPU C6-Cores [1 2 3 4 5 6 7 9 11 12 13 14 15]
30. CPU P-States [(12) 21 23 26 29 31 32 33 34 36 39]
31. CPU C6-Cores [1 2 3 4 5 6 7 8 9 11 12 13 14 15]
32. CPU P-States [(12) 21 23 26 28 29 31 32 33 34 36 39]
33. CPU C6-Cores [0 1 2 3 4 5 6 7 8 9 11 12 13 14 15]
34. CPU P-States [(12) 21 23 26 28 29 30 31 32 33 34 36 39]
35. CPU C6-Cores [0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15]
36. CPU C3-Cores [0 1 3]
37. CPU P-States [12 21 23 24 26 28 29 30 31 32 33 34 36 (38) 39]
38. CPU C3-Cores [0 1 2 3 6]
39. CPU C3-Cores [0 1 2 3 6 7]
40. CPU P-States [(12) 21 23 24 25 26 28 29 30 31 32 33 34 36 38 39]
41. CPU P-States [(12) 18 21 23 24 25 26 28 29 30 31 32 33 34 36 38 39]
42. CPU P-States [(12) 18 20 21 23 24 25 26 28 29 30 31 32 33 34 36 38 39]
43. CPU P-States [12 18 20 21 22 (23) 24 25 26 28 29 30 31 32 33 34 36 38 39]
44. CPU P-States [12 18 20 21 22 (23) 24 25 26 27 28 29 30 31 32 33 34 36 38 39]
45. CPU P-States [12 18 20 21 22 23 24 25 26 27 28 29 30 31 32 33 34 (35) 36 38 39]
46. CPU P-States [12 18 20 21 22 23 24 25 26 27 28 29 30 31 32 33 34 35 36 (37) 38 39]
47. CPU C3-Cores [0 1 2 3 6 7 10 11]
48. CPU C3-Cores [0 1 2 3 6 7 10 11]
49. CPU C3-Cores [0 1 2 3 6 7 10 11]
50. CPU C3-Cores [0 1 2 3 6 7 10 11]
51. CPU P-States [(12) 18 19 20 21 22 23 24 25 26 27 28 29 30 31 32 33 34 35 36 37 38 39]
52. CPU C3-Cores [0 1 2 3 4 6 7 10 11]
53. CPU C3-Cores [0 1 2 3 4 5 6 7 10 11]
54. CPU C3-Cores [0 1 2 3 4 5 6 7 8 9 10 11]
55. CPU C3-Cores [0 1 2 3 4 5 6 7 8 9 10 11]
56. Theos-Mac-Pro:Desktop theomitrentsis\$

Alles anzeigen

Was mach' ich falsch ?