

[Sammelthread] MacOS BigSur 11.0 DEV-Beta Erfahrungen

Beitrag von „Mork vom Ork“ vom 9. Juli 2020, 15:09

Gibt es hier jemanden, der bereits erfolgreich einen BIG SUR Bootstick auf einem X299-System erstellt und von diesem dann via OC0.6.0 erfolgreich ein CleanInstall auf seinem X299 System zum laufen gebracht hat?

Ich bekomme es weder auf meinem ASRock X299 OC Formula, noch auf dem ASUS X299 SAGE/10G so hin, als dass ich behaupten könnte, es würde "ohne Probleme" laufen.

Und bevor Fragen aufkommen: ja, ich nutze OC sowie LILU and Co. jeweils in der aktuellsten, selbst kompilierten Version. Jedoch bleibe ich bei beiden Systemen jeweils bei PCI-Configuration hängen (Kernelpanic).

Könnte also der ein oder andere von Euch, bei dem es erfolgreich geklappt hat, bitte mal seine config.plist zur Verfügung stellen? Wötmöglich habe ich ja nur den ein oder anderen Eintrag falsch gesetzt oder gar

übersehen.

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AppleRPCPU: ProcessorId=255 LocalApicId=255 Disabled
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AppleRPCPU: ProcessorId=255 LocalApicId=255 Disabled
Darwin Image4 Validator Version 3.0.0: Wed Jun 17 23:00:24 PDT 2020: root@AppleImage4-04.0.0.110.1-62/AppleImage4/RELEASE_X86_64
calling apo_policy_init for RWF
Security policy loaded: Apple Mobile File Integrity (RMF)
calling apo_policy_init for Sandbox
Security policy loaded: Seatbelt sandbox policy (Sandbox)
calling apo_policy_init for Quarantine
Security policy loaded: Quarantine policy (Quarantine)
RCPI Warning: Resetting MSR_IA32_TSC_ADJUST to 0!IOAPIC: Version 0x20 Vectors 104:111
IOAPIC: Version 0x20 Vectors 96:103
IOAPIC: Version 0x20 Vectors 64:87
IOAPIC: Version 0x20 Vectors 88:95
GPE_L6B has no implementation (28169930/evgpoint-465)
RCPI: Executed 9 blocks of module-level executable RML code
RCPI: sleep states: S3 S4 S5
HID: Legacy sh1a 2
RTC: noncontiguous device register map entries
HID: Legacy sh1a 2
VirtualSMC vsmc: @ ignoring booter rev info due to forced gen 1
pci (busid 17,5f,50 Jun 13 2020), flags 0x20c0000
[ PCI configuration begin ]
[ PCI configuration end, bridges 17, devices 29 ]
[ PCI configuration begin ]
SMCSuperIO ssio: @ starting up SuperIO sensors
AppleWME Assert failed: ( 0 != data ) ReleaseIDNode file: /AppleInternal/BuildRoot/Library/Caches/com.apple.xbs/Sources/IONWMEFamily/IONWMEFamily-540/Common/IONWMEController.cpp line: 5613
AppleWME Assert failed: ( 0 != data ) ReleaseIDNode file: /AppleInternal/BuildRoot/Library/Caches/com.apple.xbs/Sources/IONWMEFamily/IONWMEFamily-540/Common/IONWMEController.cpp line: 5613
AppleWME Assert failed: 0 == (status) Exit file: /AppleInternal/BuildRoot/Library/Caches/com.apple.xbs/Sources/IONWMEFamily/IONWMEFamily-540/Common/IONWMEController.cpp line: 5662
1997245bus AppleThunderboltGenericHRL(0)::probe
1997248bus AppleThunderboltGenericHRL(0)::start
virtual IOReturn IONWMEController::CreateSubmissionQueue(uint16_t, uint8_t)::2886:SQ Index=0 entrysize=64
virtual IOReturn IONWMEController::CreateSubmissionQueue(uint16_t, uint8_t)::2886:SQ Index=1 entrysize=64
apfs_module_start::2252: load: com.apple.filesystems.apfs, v1640.0.0, apfs-1640.0.0.111.2, 2020/06/17
apfs_module_start::2270: setting max link-id cache size to: 12
SMCSuperIO ssio: @ detected device Nuoton NCT16791D
[ PCI configuration end, bridges 22, devices 83 ]
[ PCI configuration begin ]
AppleWME Assert failed: ( 0 != data ) ReleaseIDNode file: /AppleInternal/BuildRoot/Library/Caches/com.apple.xbs/Sources/IONWMEFamily/IONWMEFamily-540/Common/IONWMEController.cpp line: 5613
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virtual IOReturn IONWMEController::CreateSubmissionQueue(uint16_t, uint8_t)::2886:SQ Index=1 entrysize=64
AppleWME Assert failed: 0 == (status) Exit file: /AppleInternal/BuildRoot/Library/Caches/com.apple.xbs/Sources/IONWMEFamily/IONWMEFamily-540/Common/IONWMEController.cpp line: 5662
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virtual IOReturn IONWMEController::CreateSubmissionQueue(uint16_t, uint8_t)::2886:SQ Index=1 entrysize=64
virtual IOReturn IONWMEController::CreateSubmissionQueue(uint16_t, uint8_t)::2886:SQ Index=0 entrysize=64
virtual IOReturn IONWMEController::CreateSubmissionQueue(uint16_t, uint8_t)::2886:SQ Index=1 entrysize=64
virtual IOReturn IONWMEController::CreateSubmissionQueue(uint16_t, uint8_t)::2886:SQ Index=0 entrysize=64
virtual IOReturn IONWMEController::CreateSubmissionQueue(uint16_t, uint8_t)::2886:SQ Index=1 entrysize=64
[ PCI configuration end, bridges 23, devices 93 ]
[ PCI configuration end, bridges 25, devices 124 ]
1955712bus AppleThunderboltGenericHRL(25)::start - status = 0x0
AppleThunderboltGenericHRL::earlyWake - complete - took 0 mlliseconds
Thunderbolt 253 PCI - LS-0x7043 LC-0x0049 SS-0x0049 SC-0x0008 PMCSR-0x0000 RT-0xffffffff MLRT-0xffffffff LWR-0xffffffff PRT-0xffffffff TRT-0x0000 TLR-0x0000 TLR-0x0000 TLR-0x0000 TLR-0x0000 TLUP-0x0001
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Danke im Voraus...

Mork vom Ork

PS: ein auf einer externen SSD auf einem MacBook installiertes BIG SUR läuft mit meinem Bootstick an beiden Rechner problemlos und fehlerfrei. Nur der Boot von einem BIG SUR Installstick will partout nicht gelingen.