

Erledigt

10.12.6 ...was mach ich falsch ?

Beitrag von „Hennings“ vom 24. September 2018, 19:27

okay...hier der stand der dinge :

ozmosis ist ja geflasht, habe den VoodooTSCSync kext für eine 8 core cpu (hab irgendwie nur welche für 4/6/8 core cpus gefunden und dann eben die mit 8 genommen), eine defaults.plist erstellt sowie alle schritte (PRAM reset) der anleitung befolgt...

der rechner bleibt an folgender stelle stehen :



```
AppleACPICPU: ProcessorId=0 LocalApicId=0 Enabled
AppleACPICPU: ProcessorId=1 LocalApicId=1 Enabled
AppleACPICPU: ProcessorId=2 LocalApicId=2 Enabled
AppleACPICPU: ProcessorId=3 LocalApicId=3 Enabled
AppleACPICPU: ProcessorId=4 LocalApicId=4 Enabled
AppleACPICPU: ProcessorId=5 LocalApicId=5 Enabled
AppleACPICPU: ProcessorId=6 LocalApicId=6 Enabled
AppleACPICPU: ProcessorId=7 LocalApicId=7 Enabled
AppleACPICPU: ProcessorId=8 LocalApicId=8 Enabled
AppleACPICPU: ProcessorId=9 LocalApicId=9 Enabled
AppleACPICPU: ProcessorId=10 LocalApicId=10 Enabled
AppleACPICPU: ProcessorId=11 LocalApicId=11 Enabled
AppleACPICPU: ProcessorId=12 LocalApicId=12 Enabled
AppleACPICPU: ProcessorId=13 LocalApicId=13 Enabled
AppleACPICPU: ProcessorId=14 LocalApicId=14 Enabled
AppleACPICPU: ProcessorId=15 LocalApicId=15 Enabled
calling sso_policy_init for TMSafetyNet
Security policy loaded: Safety net for Time Machine (TMSafetyNet)
calling sso_policy_init for AppI
Security policy loaded: Apple Mobile File Integrity (AMFI)
calling sso_policy_init for Sandbox
Security policy loaded: Seatbelt sandbox policy (Sandbox)
calling sso_policy_init for Quarantine
Security policy loaded: Quarantine policy (Quarantine)
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MPC Framework successfully initialized
using 16384 buffer headers and 10240 cluster: 32 buffer headers
AppleCredentialManager::start called
AppleKeyStore starting (0x1011) Jun 13 2017 18:08:56)
Unknown CPU family = 0x0, model = 0x0, stepping = 0x7
OSMPIC: Version 0x20 Vectors 04:07
RCP1: Created 1 blocks of module-level executable ARM code
RCP1: sleep status S3 S4 S5
ms: build 1740:32 Jun 13 2017). Flags 0x0000, pf04 (44 sps) 0x77f0000000, 0x00000000
[ pci configuration begin ]
```