

Intel Speedstep CPU

Beitrag von „rubenszy“ vom 7. September 2017, 12:34

Sieht nicht mal so verkehrt aus.

Werte der SSDT erstellt mit ssdtPRGen.sh

```
CPU Ratio Info:
-----
Base Clock Frequency (BLCK).....: 100 MHz
Maximum Efficiency Ratio/Frequency.....: 8 ( 800 MHz)
Maximum non-Turbo Ratio/Frequency.....: 31 (3100 MHz)
Maximum Turbo Ratio/Frequency.....: 39 (3900 MHz)
P-State ratio * 100 = Frequency in MHz
-----
CPU P-States [ (35) 36 ]
CPU C3-Cores [ 2 3 6 ]
CPU C6-Cores [ 0 1 2 3 4 ]
CPU C7-Cores [ 0 1 2 4 ]
CPU P-States [ (8) 32 35 36 ]
CPU C3-Cores [ 0 1 2 3 5 6 7 ]
CPU C6-Cores [ 0 1 2 3 4 6 ]
CPU C7-Cores [ 0 1 2 3 4 5 6 ]
CPU P-States [ 0 31 32 35 (36) ]
CPU C3-Cores [ 0 1 2 3 4 5 6 7 ]
CPU C6-Cores [ 0 1 2 3 4 5 6 ]
CPU C7-Cores [ 0 1 2 3 4 5 6 7 ]
CPU P-States [ 0 30 (31) 32 35 36 ]
CPU C6-Cores [ 0 1 2 3 4 5 6 7 ]
CPU P-States [ 0 30 31 32 35 36 (38) ]
CPU P-States [ 0 28 30 (31) 32 35 36 38 ]
CPU P-States [ 0 27 28 30 (31) 32 35 36 38 ]
CPU P-States [ (8) 25 27 28 30 31 32 35 36 38 ]
CPU P-States [ (8) 25 27 28 30 31 32 33 35 36 38 ]
CPU P-States [ (8) 25 27 28 30 31 32 33 34 35 36 38 ]
```

Werte des CPUFriendDataProvider.kext

```
CPU Ratio Info:
-----
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P-State ratio * 100 = Frequency in MHz
-----
CPU P-States [ (31) 32 35 ]
CPU C3-Cores [ 0 1 6 7 ]
CPU C6-Cores [ 0 1 ]
CPU C7-Cores [ 0 1 5 6 7 ]
CPU P-States [ 17 (31) 32 35 ]
CPU C3-Cores [ 0 1 3 5 6 7 ]
CPU C7-Cores [ 0 1 2 3 5 6 7 ]
CPU P-States [ 17 22 (31) 32 35 ]
CPU P-States [ 17 19 22 (31) 32 35 ]
CPU P-States [ (8) 17 19 22 23 31 32 35 ]
CPU C3-Cores [ 0 1 3 4 5 6 7 ]
CPU C7-Cores [ 0 1 2 3 4 5 6 7 ]
CPU P-States [ 0 17 19 22 23 24 (31) 32 35 ]
CPU P-States [ 0 17 19 20 22 23 24 (31) 32 35 ]
CPU P-States [ 0 17 19 20 21 22 23 24 (31) 32 35 ]
CPU C3-Cores [ 0 1 2 3 4 5 6 7 ]
CPU P-States [ 0 17 19 20 21 22 23 24 (31) 32 34 35 ]
CPU P-States [ 0 17 18 19 20 21 22 23 24 (31) 32 34 35 ]
CPU P-States [ (8) 15 17 18 19 20 21 22 23 24 31 32 34 35 ]
CPU P-States [ (8) 15 17 18 19 20 21 22 23 24 27 31 32 34 35 ]
CPU P-States [ (8) 15 17 18 19 20 21 22 23 24 25 27 31 32 34 35 ]
CPU P-States [ (8) 15 17 18 19 20 21 22 23 24 25 27 29 31 32 34 35 ]
CPU P-States [ 0 15 17 18 19 20 21 22 23 24 25 27 29 30 (31) 32 34 35 ]
CPU P-States [ 0 15 17 18 19 20 21 22 23 24 25 26 27 28 29 30 31 32 34 35 (38) ]
CPU P-States [ 0 15 17 18 19 20 21 22 23 24 25 26 27 28 29 30 31 32 34 35 (36) 38 ]
CPU P-States [ 0 15 16 17 18 19 20 21 22 23 24 25 26 27 28 29 30 (31) 32 34 35 36 38 ]
CPU C6-Cores [ 0 1 4 5 ]
```

Werte der ssdt_data.dsl

```

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-----
Base Clock Frequency (BLCK)..... : 100 Mhz
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Maximum Turbo Ratio/Frequency.....: 39 (3900 Mhz)
P-State ratio * 100 = Frequency in Mhz
-----
CPU P-States [ (31) 33 38 ]
CPU C3-Cores [ 0 2 3 ]
CPU C6-Cores [ 0 1 2 6 7 ]
CPU C7-Cores [ 0 1 2 6 7 ]
CPU P-States [ 28 (31) 33 38 ]
CPU C3-Cores [ 0 2 3 4 ]
CPU C6-Cores [ 0 1 2 3 6 7 ]
CPU C7-Cores [ 0 1 2 5 6 7 ]
CPU P-States [ (8) 28 29 31 33 38 ]
CPU C3-Cores [ 0 2 3 4 6 7 ]
CPU C7-Cores [ 0 1 2 3 5 6 7 ]
CPU P-States [ (8) 27 28 29 31 33 38 ]
CPU C3-Cores [ 0 1 2 3 4 5 6 7 ]
CPU P-States [ (8) 27 28 29 30 31 33 38 ]
CPU P-States [ (8) 27 28 29 30 31 33 35 38 ]
CPU C7-Cores [ 0 1 2 3 4 5 6 7 ]
CPU P-States [ (8) 27 28 29 30 31 33 35 38 (39) ]
CPU P-States [ (8) 27 28 29 30 31 32 33 35 38 39 ]
CPU P-States [ (8) 27 28 29 30 31 32 33 34 35 38 39 ]
CPU P-States [ (8) 26 27 28 29 30 31 32 33 34 35 38 39 ]
CPU P-States [ (8) 26 27 28 29 30 31 32 33 34 35 (36) 38 39 ]
CPU P-States [ (8) 26 27 28 29 30 31 32 33 34 35 (36) 37 38 39 ]
CPU P-States [ (8) 25 26 27 28 29 30 31 32 33 34 35 36 37 38 39 ]
CPU P-States [ (8) 23 25 26 27 28 29 30 31 32 33 34 35 36 37 38 39 ]
CPU P-States [ (8) 23 24 25 26 27 28 29 30 (31) 32 33 34 35 36 37 38 39 ]
CPU C6-Cores [ 0 1 2 3 4 5 6 7 ]

```

Mal durch testen was besser ist.